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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/719,589	11/21/2003	Pete Sivonen	875.0127.U1(US)	3270
29683	7590	03/10/2005	EXAMINER	
HARRINGTON & SMITH, LLP			CHOE, HENRY	
4 RESEARCH DRIVE			ART UNIT	
SHELTON, CT 06484-6212			PAPER NUMBER	
			2817	

DATE MAILED: 03/10/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/719,589

Applicant(s)

SIVONEN ET AL.

Examiner

Henry K. Choe

Art Unit

2817

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 21 November 2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4, 8, 12-17, 20-23 and 27 is/are rejected.
- 7) ☒ Claim(s) 5-7, 9-11, 18, 19 and 24-26 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|----------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>1/26/2004</u> | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-4, 8, 12-17, 20-23 and 27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Webb (Fig. 3A).

Regarding claims 1, 8, 15, 20 and 27, Webb (Fig. 3A) discloses an amplifier circuit comprising a resonant load circuit (2K, 33K, -2, 0.01uF; They are connected to a collector of transistor Q5) comprising an inductance (-2) in parallel with a capacitance (0.01uF) further comprising a first resistance Rs (2K) in series with the inductance (-2), and a second resistance Rp (33K) in parallel with the inductance (-2) and capacitance (0.01uF). As described above, Webb (Fig. 3A) discloses all the limitations in the claims except for that the first and second integrated resistances having values selected for reducing an amount of resonant load circuit Q and the resonant load circuit disposed in an integrated circuit. It would have been obvious to one of ordinary skill in the art at the time the invention was made to have implemented the specific values of the resistors, since they are based on the routine experimentation to obtain the optimum operating parameters. Furthermore, it is well known to those of ordinary skill in the art to integrate a semiconductor device in order to form a small sized integrated Circuit. Therefore, it would have been obvious to have integrated the circuit of the reference [Webb (Fig. 3A)] because such a modification would have advantageously produced a small size integrated circuit amplifier.

Regarding claims 2, 12 and 21, the first resistance (2K) is connected in series with the inductance (-2).

Regarding claims 3, 13 and 22, the resonant load circuit (2K, 33K, -2, 0.01uF) forms a load in an RF amplifier (Fig. 3A).

Regarding claims 4, 14, 16, 17 and 23, the limitations recited in the claims are obvious based on the intended use of the invention.

Allowable Subject Matter


Claims 5-7, 9-11, 18, 19 and 24-26 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Patent numbers (3,210,681; 3,486,126) are the amplifiers with the load resonant circuits.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Henry Choe whose telephone number is (571) 272-1760.


HENRY CHOE
PRIMARY EXAMINER

#986